General Information

Lunch is served in the foyer from 11:30-12:15.

Conference presentations start in the Room „Aula“ at 12:30 sharp.

General Chair

Prof. Dr.-Ing. Mladen Berekovic
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Workshop Chair

Dr. Carsten Trinitis
E-Mail: Carsten.Trinitis@tum.de
2nd Workshop on Computer Architectures in Space (CompSpace)
Room: Veolia

Chairs:
Martin Langer, Carsten Trinitis, Dai Yang
TU Munich, Germany

Session 1: 08:30-10:00
Verification of the C++-Operating System RODOS in Context of a Small-Satellite
Rainer Gerlich, Ralf Gerlich, Karsten Gordon, Merlin Barschke, Sergio Montenegro, Erik Dilger, Frank Flederer

The Use of Different Architectures and Streak Observations Algorithms to Detect Space Debris
Gerard Vives Valdduriola, Diego Andrés Suárez Trujillo, Tim Helfers, Damien Daens, Arthur Scharf, Jens Utzmann, Jean-Noel Pittet, Alessandro Vananti

Generating SEL and SEU with a class 1 laser setup – extensions and further investigations relating applicability
Hannes Zöllner, Thomas Brömel, Burkart Voß

Coffee break

Session 2: 10:30-11:30
COMET: a Configuration Memory Tool to Analyze, Visualize and Manipulate FPGAs Bitstream
Ludovica Bozzoli, Luca Sterpone

The Flying Laptop Satellite as a Low-Threshold Testbed for Computing in Orbit
Phillip Hagel, Jonas Burgdorf, Sebastian Wenzel

14th GI/ITG Workshop on Dependability and Fault Tolerance (VERFE)
Room: Weitblick

Chairs:
Bernhard Fechner, Univ. Hagen, Germany
Karl-Erwin Großpietsch, St. Augustin, Germany

Session 1: 08:30-10:00
General Extension of FABAN and a Topology-based Approach to tolerate any Number of Faults
Valentin Fitz, Klaus Echtle

Detection of Wrongly Directed Messages by Efficient Fault-Tolerance Signatures
Klaus Echtle

Model-based Security Engineering for Secure Systems Development
Armin Lunkeit, Hartmut Pohl

Coffee break

Session 2: 10:30-11:30
Applying the Ideal Testing Framework to HDL Programs
Onur Kilincceker, Ercument Turk, Moharram Challenger, Fevzi Belli

Compressed Low-rate Codes for Failure-tolerant Distributed Storage
Peter Sobe